## **Attachments:** (Include any other document)

## a. Diagrams/Drawings/Blueprints

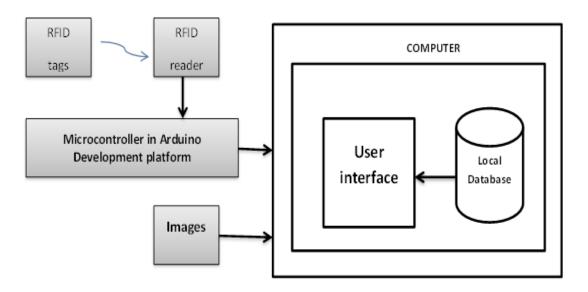
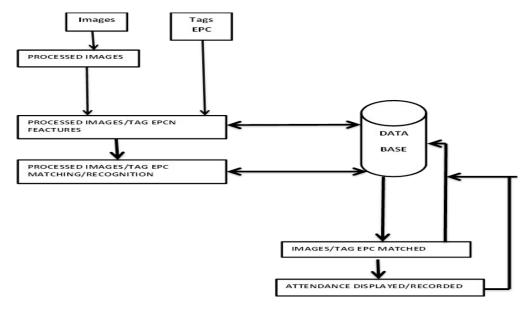


Figure 3: the system block diagram



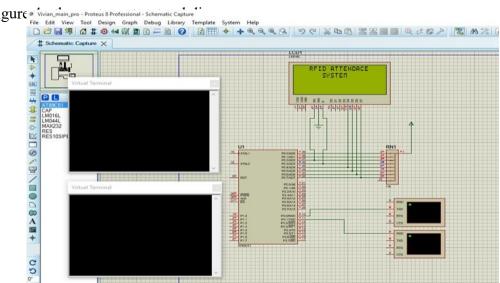


Figure 5: the system circuit diagram

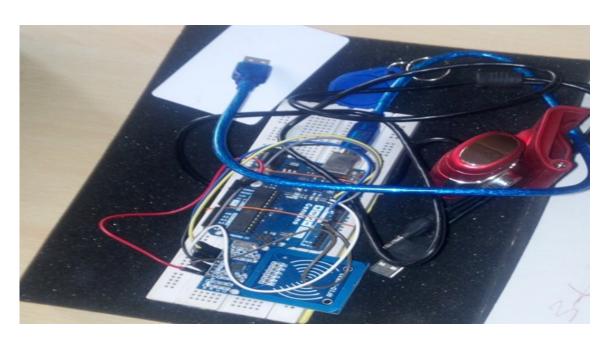


Figure 6: the hardware design

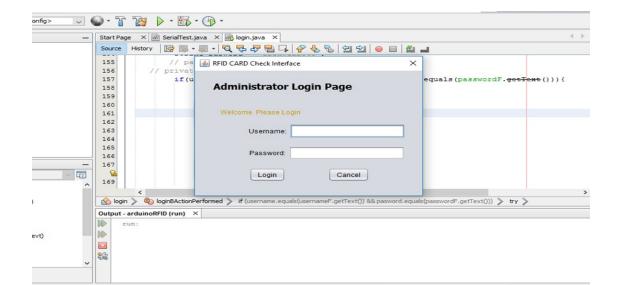


Figure 7: the admin login

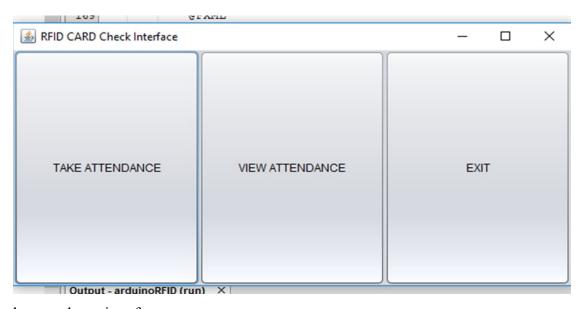


Figure 8: the attendance interface

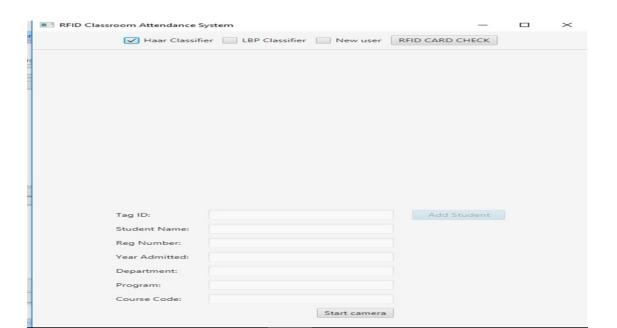


Figure 9: the registration and image processing interface

D CI	assroom Attendance S	System			·	
	✓ Haar Classifi	ier BP Classifier	✓ New user	RFID CARD CH	HECK	
	Tag ID:	165		Add Stu	ident	
	Tag ID: Student Name:	165 ezekwe chinwe		Add Stu	ident	
				Add Stu	ident	
	Student Name:	ezekwe chinwe		Add Stu	dent	
	Student Name: Reg Number: Year Admitted:	ezekwe chinwe 2012/05906 2012		Add Stu	dent	
	Student Name: Reg Number: Year Admitted: Department:	ezekwe chinwe 2012/05906 2012 computer science		Add Stu	odent	
	Student Name: Reg Number: Year Admitted:	ezekwe chinwe 2012/05906 2012		Add Stu	dent	
	Student Name: Reg Number: Year Admitted: Department:	ezekwe chinwe 2012/05906 2012 computer science		Add Stu	dent	
	Student Name: Reg Number: Year Admitted: Department: Program:	ezekwe chinwe 2012/05906 2012 computer science phd	Start camera		ident	

Figure 10: the registration in progress

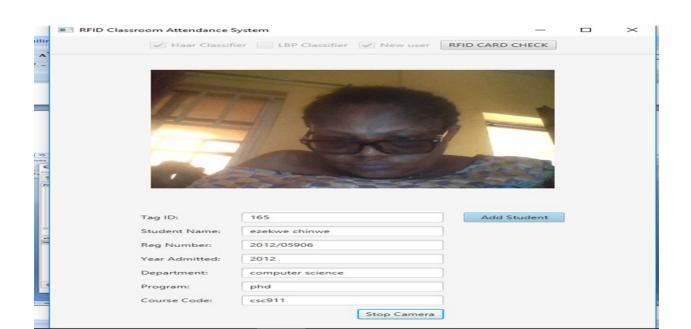


Figure 11: image training

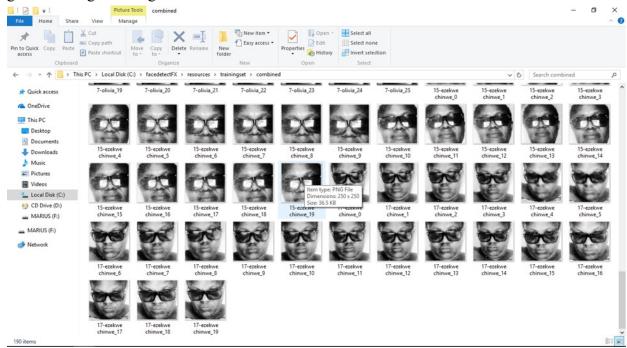


Figure 12: different face eigen factors generated after training

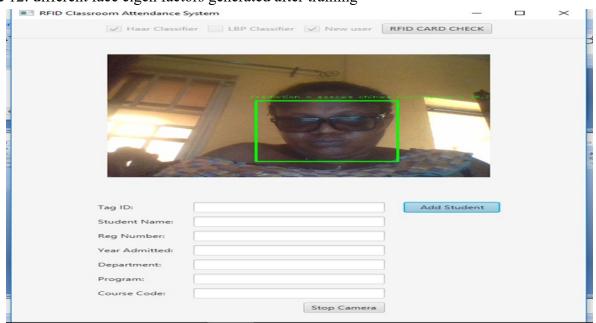


Figure 13: face eigen factors recognition for taking attendance

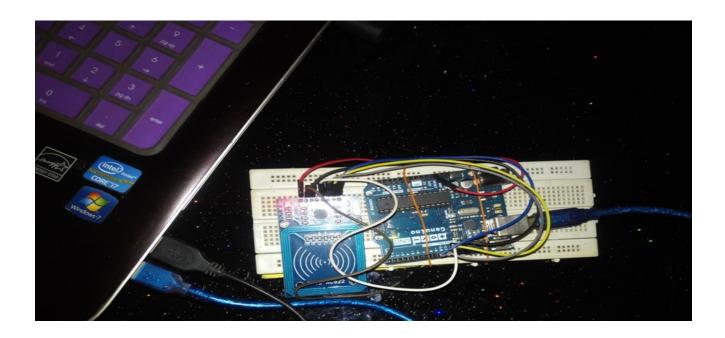


Figure 14: hardware interfaced ready for taking attendance

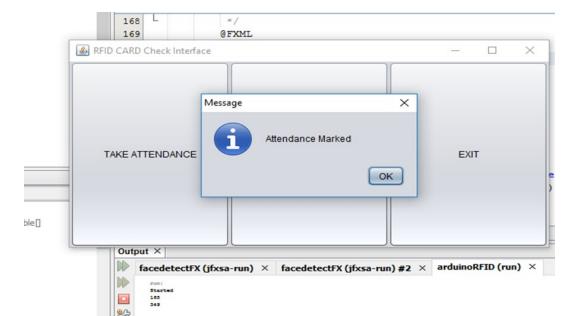


Figure 15: attendance taken with the right tag EPC and face eigen factors

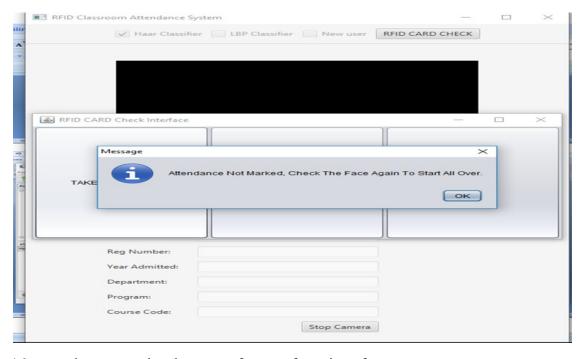


Figure 16: attendance not taken because of wrong face eigen factors